

CURRICULUM VITAE



DEEPMALA SRIVASTAVA

Designation: Assistant Professor

Department: Electronics & Communication

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ACADEMIC/INDUSTRIAL EXPERIENCE:-

- 1) Working as Assistant Professor in BBD Group , Lucknow from 6 th August 2008 onwards.
- 2) Worked as **Trainee Engineer** (VLSI and Embedded System) in **Semiconductor Complex Ltd (SCL-SKIT)- Vedant** , Lucknow from 1st April 2005 to 31st July 2006.

QUALIFICATION: -

M.Tech (VLSI Design)

AREA OF INTEREST:-

VLSI Design, Embedded System, Microprocessor, Microcontroller, Digital Electronics, VHDL & Verilog

RESEARCH PUBLICATION:-

- 1) Deepmala Srivastava, Rabindra Kumar Singh, "Ultra-Low Power Heterojunction Dopingless -Tunnel FET (HD-TFET) Design and Characterization with SiO₂/HfO₂ Gate Stacking for High Current Drive", *Turkish Journal of Computer and Mathematics Education, Vol.12 No.12 (2021), 767-780*
- 2) Deepmala Srivastava "A Survey on Emerging Nanoelectronics Devices" in proc. Of conf. "COTII'13" held in Ambalika Institute of Management & Technology, Lucknow.

FDP / FTP / CONFERENCES ATTENDED:-

- 1) Participated in the 5 days Faculty Development Program on "Recent Advancements in Semiconductor Technologies – RASET 20" during **9th – 14th October 2020** organized by Department of Electronics and Communication Engineering, SRMIST, Chennai.
- 2) FDP on Data Analysis Using Advanced Excel, IIT Kanpur: 2022
- 3) Participated in the 5 days FDP – cum - STTP on Simulation, Modelling and Application of Advanced Semiconductor Devices from 05- 09 July, 2021, organized by the

Department of Electronics Engineering, Harcourt Butler Technical University, Kanpur, Uttar Pradesh, India - 208002.

- 4) Participated in one week FDP on "IoT over Edge and Cloud Computing-1.0 (IECC-1.0)" jointly organized by Department of Electronics & Communication Engineering, BBDNIIT, along with Learn & Build from 14th June - 19th June 2021.
- 5) Participated three days Faculty Development Program on "Outcome based Education", organized by Department of Information Technology, Dr. Akhilesh Das Gupta Institute of Technology & Management, during 14th-16th September 2020.
- 6) Participated in the Faculty Development Program on "Computational Intelligence : COIN-2020" organized by Babu Banarasi Das Northern India Institute of Technology, Lucknow on from **June 26 to 27, 2020.**
- 7) Participated in the Faculty Development Program on "Role of LaTeX in Scientific Writing" organized by Department of Mathematics, Babu Banarasi Das Northern India Institute of Technology, Lucknow from **June 19 to 21, 2020.**
- 8) Participated in the Faculty Development Program on "Trends in Technological Intelligence-T2I 2020" organized by IEI Chapter of Department Electronics & Communication Engineering & Department of Electrical Engineering, BBDNIIT in association with The Institution of Engineers (India) from **May 31 to June 01, 2020.**
- 9) Participated in the 5 days Faculty Development Program on "Robotics" organized by Department of Mechanical Engineering, IIT Kanpur under TEQIP-III from **January 21-25 January 2020.**
- 10) Participated in Eight day faculty development Program on 'Universal Human Values and Professional Ethics' organized by TEQIP-III and conducted by value education cell, AKTU Lucknow at BBDNITM, Lucknow **from 21st -28th Dec 2018.**
- 11) Participated in one week Faculty Development Program on "Emerging Trends in Computational Intelligence" Organized by BBDNIIT, Lucknow and sponsored by AKTU, Lucknow **from June 18-22, 2018.**
- 12) Participated in the eight days Faculty Development Program on Information Storage Management and Introduction to big data Analytics" organized by the Department of Computer Science & Engineering, ASET, Amity University, Lucknow from **9-16 Feb 2018.**
- 13) Participated in the three days Faculty Development Program on "**Microcontroller Design Workshop" on Texas Instruments' MSP430 microcontroller platform,** organized by Electronics Engineering Department of **Institute of Engineering & Technology (IET),** Lucknow during 24-26 August 2017
- 14) Participated in the three days workshop on "**Familiarization Workshop on Nanofabrication Technologies"** organized by **IIT Bombay** during May 25-27, 2016.
- 15) Participated the workshop on "Advances in Electrical Engineering -2015" organized by Electrical Engineering Department of **Institute of Engineering and Technology,** Lucknow
- 16) Participated the one day workshop on Advancements in Research Methodology organized by SITM, Barabanki.

- 17) Participated a two days workshop on “Embedded System using MSP 430’ organized by ECE department, BBDNIIT, Lucknow during 5-6th January 2018.
- 18) Participated two days workshop on “Digital Image Processing and Augmented Reality” organized by ECE department, BBDNIIT, Lucknow during 30-31st August 2017.
- 19) Participated in Faculty Development Program on “Cloud Computing-The Road Ahead & Trends in IT- Beyond ADM ” held on 22nd July,2011, organized by TCS ,Lucknow.
- 20) Attended the Seminar “MATLAB & Simulink for Engineering Education” organized by Mathworks India, held on 16 September, 2011 .
- 21) Participated the National Workshop on “Challenges in VLSI” organized at DA- IICT, Gandhinagar during December 21-22, 2006
- 22) Participated in the section of Physics during the 89th session of the Indian Science Congress held at Lucknow University from January 3 to 7, 2002.
- 23) Summer Training in Telephone Network Switching System at Bharat Sanchar Nigam Limited , Sitapur (U.P) during 19th May to 18th June 2003.
- 24) **Lifetime membership of IETE (The Institution of Electronics and Telecommunication Engineers)**

ACHIEVEMENTS:-

- NPTEL Online Certification 12 week (Jul-oct2018) course on Digital Circuits.
- Shortlisted as NPTEL Translator (English to Hindi).

OTHER RESPONSIBILITIES:

- 1) Department Placement Coordinator
- 2) Class Coordinator

Date: 06 JUNE 2022

Place: Lucknow

BBDNIIT

(Deepmala Srivastava)