

## CURRICULUM VITAE



### SHIV PRAKASH SINGH

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Department: Electrical Engineering  
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**ACADEMIC/INDUSTRIAL EXPERIENCE:-** 18 years

**QUALIFICATION:-** M.Tech (Power Electronics and Drives)  
B.Tech (Applied Electronics and Instrumentation Engineering)

**AREA OF INTEREST:-** Power Electronics, Smart Grid, Renewable Energy

#### RESEARCH PUBLICATION:-

- Shiv Prakash Singh, K.P.Singh, A.N.Thirul, "Analysis and Simulation of Sepic Integrated boost Converter (SHI Converter)", Global Journal of Computer Application & Technology (GJCAT), Vol. 2 (1), page 803-805, 2012.
- Shiv Prakash Singh, K.P.Singh, "A Review Paper On Grid Connected PV Inverter", National Conference on Emerging Trends in Electrical Systems (ETES-14) MMBUT Gorakhpur, March 28-29, 2014.
- Shiv Prakash Singh, K.P.Singh, "Elimination Of Harmonics in Power System By The Combination Of Passive And Active Filter With P-Q Control Technique", National Conference on Emerging Trends in Electrical Systems (ETES-14) MMBUT Gorakhpur, March 28-29, 2014.
- Published Research paper on "Harmonic Distortion & Mitigation at Distribution Side of Power System with Ability of Harmonic Prediction", in International Conference on Industry 4.0 at Dr. Ram Manohar Lohia Avadh University, September 28-29, 2019.
- Published Research paper on "A review on Harmonic Distortion at Distribution Side with Distributed Generation in Power System", in International Conference on Industry 4.0 at Dr. Ram Manohar Lohia Avadh University, September 28-29, 2019.
- Published Research paper on "Voltage Stability Enhancement of Power System Network using STATCOM", in International Conference at Rajkiya Engineering College, Ambedkar Nagar, from November 28-29, 2019.
- Published Research paper on "Maximization of Power Efficiency Based Optimal Location of DPFCC", in International Conference at Rajkiya Engineering College, Ambedkar Nagar, from November 28-29, 2019.

#### BOOK PUBLICATION (if Any):-